

ABSTRACT OF THE DISCLOSURE

A data processor that increases transmission efficiency. The data processor communicates data to and from
5 a host computer via an interface with a packet including a packet ID. The data processor includes a packet recognition circuit for receiving the packet including the packet ID from the host computer and recognizing the type of the packet from the packet ID. A packet length measuring circuit
10 measures packet length of the packet received from the host computer and determines whether the measured packet length is in accordance with the packet type recognized by the packet determination circuit.